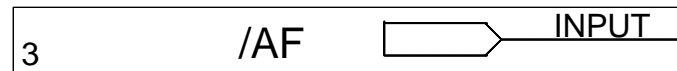
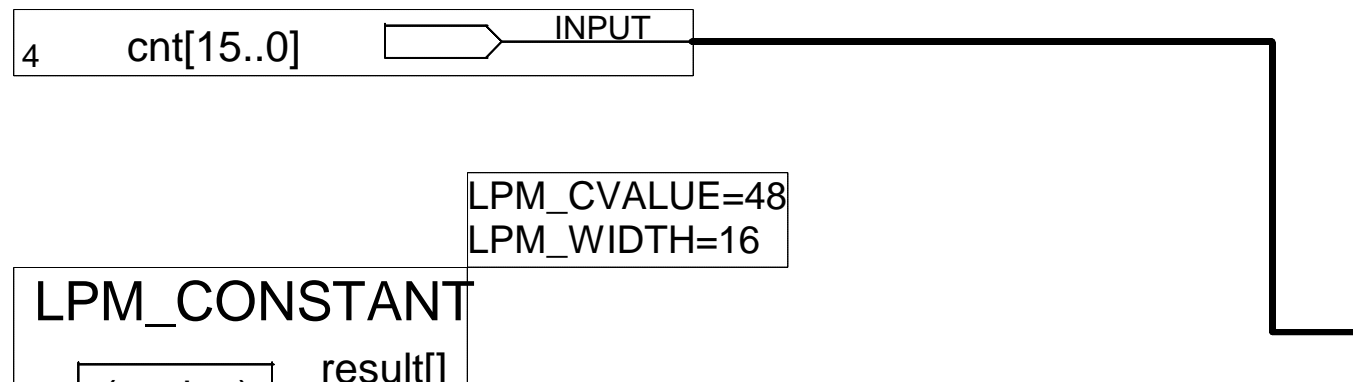


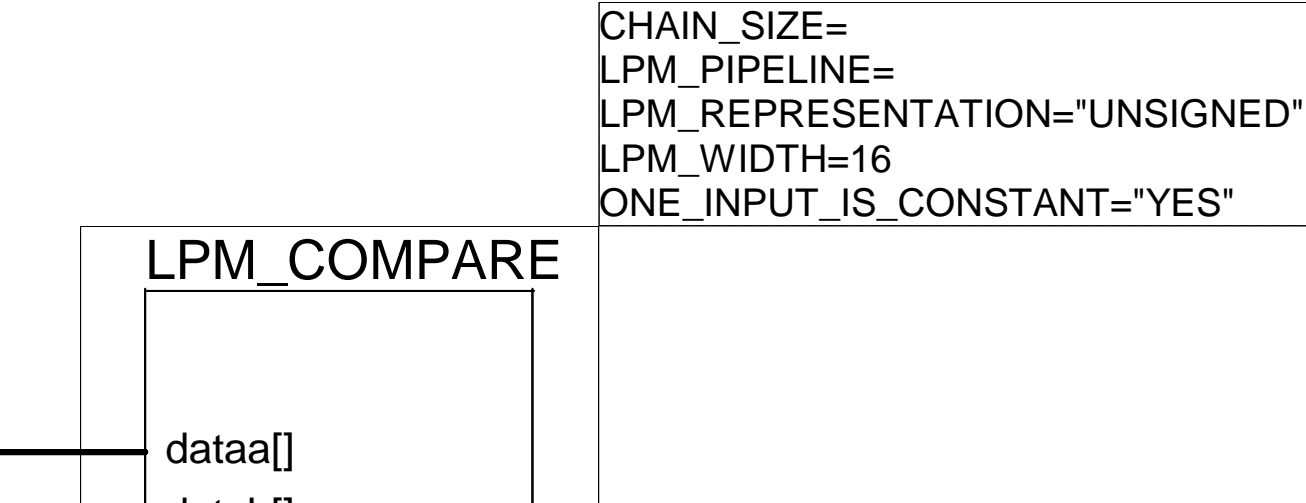
Another method can use the /AF (almost full) flag directly control the /FIFO_RD line.

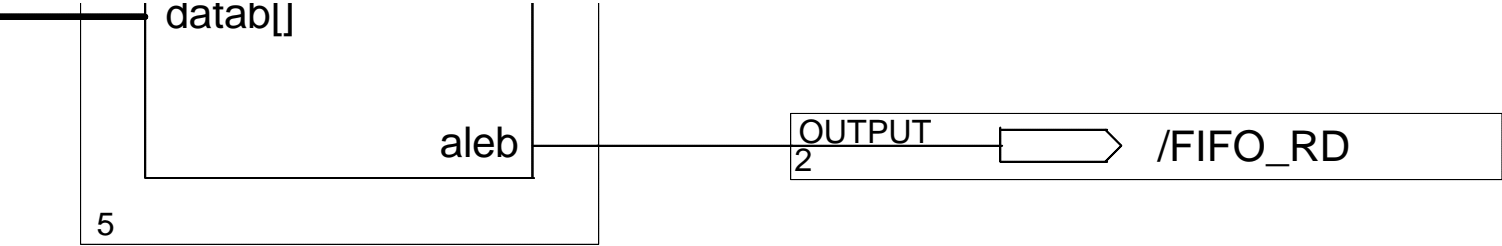


Interface to the IDT FIFO that limits the
The FIFO is not read by beam clock be
be disabled until "/FIFO_RD" goes low.
after the specified number of data values
into the FIFO.



depth of the FIFO.
because reading will
It will go low only
es have been pushed





of the IDT fifo to

TITLE		IDT FIFO Interface			
COMPANY		ORNL			
DESIGNER		Ganesh S. Rao			
SIZE	B	NUMBER	1.00	REV	A
DATE	4:29p 3-17-1998			SHEET	1 OF 1
8					